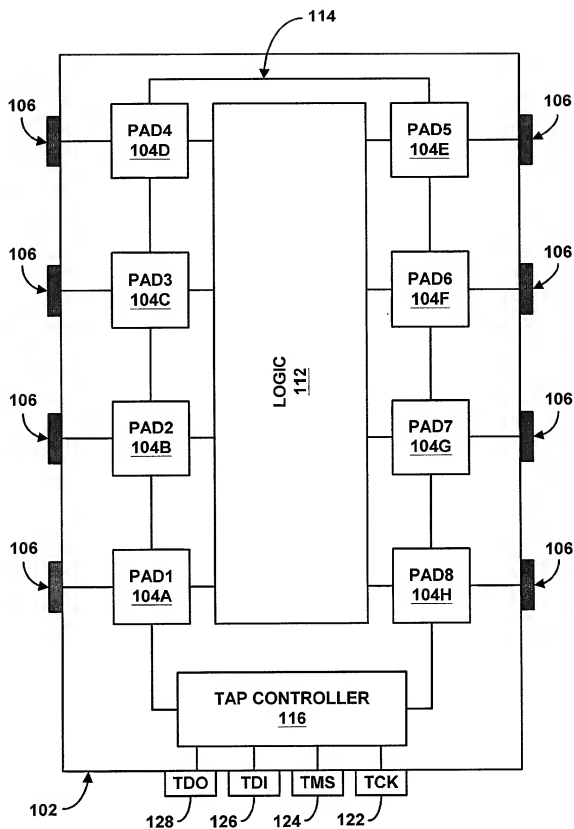
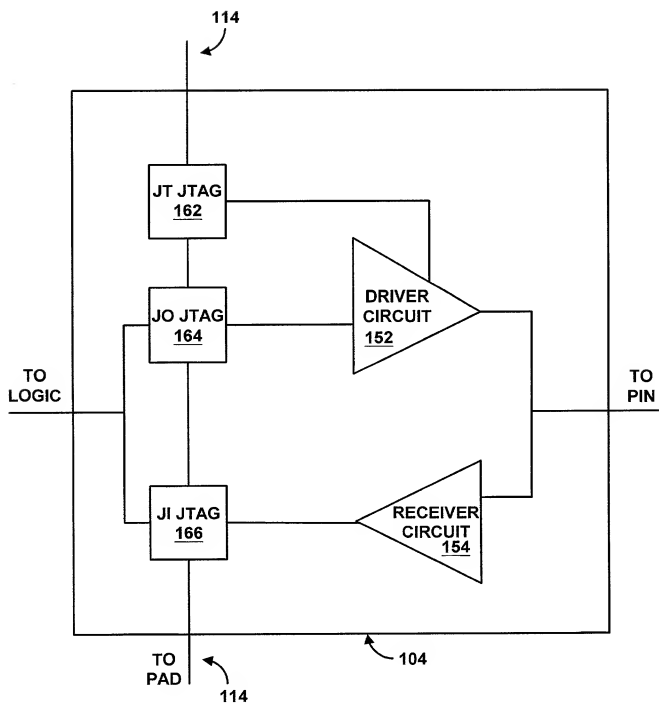
**FIG. 1**

**FIG. 2**

**FIG. 3**

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A FLAT NETLIST IS CREATED
FOR THE IC

202

TRaverse LOGIC UNTIL
JTAG REGISTER IS
LOCATED

204

STORE NAME ASSIGNED TO
JTAG REGISTER

206

ADDITIONAL
JTAG REGISTERS?

208

DETERMINE AND
DOCUMENT RELATIONSHIPS
BETWEEN JTAG REGISTER
AND I/O PIN

212

BSDL FILE IS CREATED

214

FIG. 4

YES

NO